

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being transmitted via the Office electronic filing system in accordance with § 1.6(a)(4).

Dated: 11/10/04 Signature:

(Laura C. Brutman)

Docket No.: S0193.0013  
(PATENT)

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Letters Patent of:  
Marcus Janke et al.

Patent No.: 7,290,289

Issued: October 30, 2007

For: Processor with several calculating units

### **REQUEST FOR CERTIFICATE OF CORRECTION PURSUANT TO 37 CFR 1.323**

Attention: Certificate of Correction Branch  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Upon reviewing the above-identified patent, Patentee noted typographical errors which should be corrected.

On the front page of the patent:

Under Other Publications, line 3, "Chip-Multprocessor" should read --Chip-Multiprocessor--

In the Specification:

At column 2, line 16, "mode, of" should read --mode of--

At column 4, line 48, "FIG." should read --FIGS.--

At column 6, line 55, "FIG." should read --FIGS.--

At column 6, line 64, "unit" should read --units--

At column 7, line 57, "FIG." should read --FIGS.--

The errors were found in the application as filed by applicant. Please charge our Credit Card in the amount of \$100.00 covering the fee set forth in 37 CFR 1.20(a). Credit Card Payment Form SB-2038, with a signature from an authorized cardholder, is enclosed.

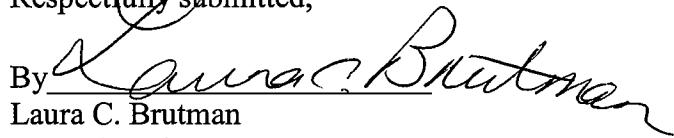
The errors now sought to be corrected are inadvertent typographical errors the correction of which does not involve new matter or require reexamination.

Transmitted herewith is a proposed Certificate of Correction effecting such amendment. Patentee respectfully solicits the granting of the requested Certificate of Correction.

The Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Deposit Account No. 50-2215, under Order No. S0193.0013. A duplicate copy of this paper is enclosed.

Dated: November 30, 2007

Respectfully submitted,

By   
Laura C. Brutman

Registration No.: 38,395  
DICKSTEIN SHAPIRO LLP  
1177 Avenue of the Americas  
New York, New York 10036-2714  
(212) 277-6500  
Attorney for Applicant

**UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION**

Page 1 of 1

PATENT NO. : 7,290,289  
APPLICATION NO. : 10/764,265  
ISSUE DATE : October 30, 2007  
INVENTOR(S) : Marcus Janke et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the front page of the patent:

Under Other Publications, line 3, "Chip-Multprocessor" should read --Chip-Multiprocessor--

In the Specification:

At column 2, line 16, "mode, of" should read --mode of--

At column 4, line 48, "FIG." should read --FIGS.--

At column 6, line 55, "FIG." should read --FIGS.--

At column 6, line 64, "unit" should read --units--

At column 7, line 57, "FIG." should read --FIGS.--

MAILING ADDRESS OF SENDER (Please do not use customer number below):

Laura C. Brutman  
DICKSTEIN SHAPIRO LLP  
1177 Avenue of the Americas  
New York, New York 10036-2714